

# LM1085 **3A Low Dropout Positive Regulators General Description**

The LM1085 is a series of low dropout positive voltage regulators with a maximum dropout of 1.5V at 3A of load current. It has the same pin-out as National Semiconductor's industry standard LM317.

The LM1085 is available in an adjustable version, which can set the output voltage with only two external resistors. It is also available in three fixed voltages: 3.3V, 5.0V and 12.0V. The fixed versions integrate the adjust resistors.

The LM1085 circuit includes a zener trimmed bandgap reference, current limiting and thermal shutdown.

The LM1085 series is available in TO-220 and TO-263 packages. Refer to the LM1084 for the 5A version, and the LM1086 for the 1.5A version.

#### Features

- Available in 3.3V, 5.0V, 12V and Adjustable Versions
- Current Limiting and Thermal Protection
- Output Current
- Line Regulation
- Load Regulation

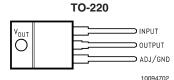
ЗA 0.015% (typical)

0.1% (typical)

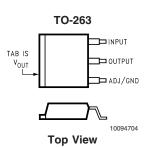
### Applications

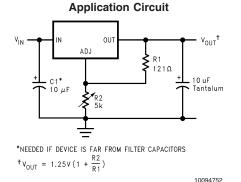
- High Efficiency Linear Regulators
- Battery Charger
- Post Regulation for Switching Supplies
- Constant Current Regulator
- Microprocessor Supply

## **Connection Diagrams**



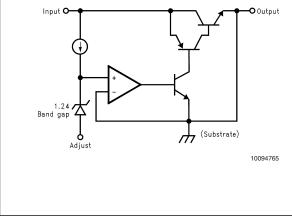








### **Basic Functional Diagram**, Adjustable Version

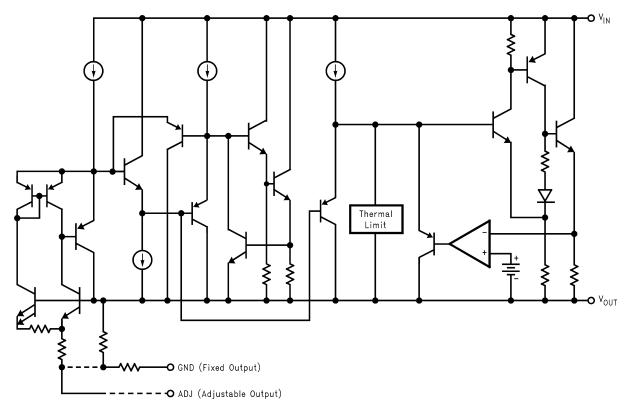


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## **Ordering Information**

Package	Temperature Range	Part Number	Transport Media	NSC Drawing
3-lead TO-263	-40°C to +125°C	LM1085IS-ADJ	Rails	_
		LM1085ISX-ADJ	Tape and Reel	
		LM1085IS-12	Rails	ТЅЗВ
		LM1085ISX-12	Tape and Reel	1000
		LM1085IS-3.3	Rails	
		LM1085ISX-3.3	Tape and Reel	
		LM1085IS-5.0	Rails	
		LM1085ISX-5.0	Tape and Reel	
3-lead TO-220	–40°C to + 125°C	LM1085IT-ADJ	Rails	- тозв
		LM1085IT-12	Rails	
		LM1085IT-3.3	Rails	1036
		LM1085IT-5.0	Rails	

# Simplified Schematic



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### Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Maximum Input to Output Voltage Differential

LM1085-ADJ	29V
LM1085-12	18V
LM1085-3.3	27V
LM1085-5.0	25V
Power Dissipation (Note 2)	Internally Limited

Junction Temperature  $(T_{,l})$  (Note 3) 150°C Storage Temperature Range -65°C to 150°C Lead Temperature 260°C, to 10 sec ESD Tolerance (Note 4) 2000V

### **Operating Ratings** (Note 1)

Junction Temperature Range (	T <sub>J</sub> ) (Note 3)
Control Section	–40°C to 125°C
Output Section	-40°C to 150°C

### **Electrical Characteristics**

Typicals and limits appearing in normal type apply for  $T_J = 25^{\circ}C$ . Limits appearing in **Boldface** type apply over the entire junction temperature range for operation.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
V <sub>REF</sub> Reference Voltage	Reference Voltage	LM1085-ADJ		, ,	, ,	
		$I_{OUT} = 10 \text{mA}, V_{IN} - V_{OUT} = 3 \text{V}$	1.238	1.250	1.262	V
	$10mA \le I_{OUT} \le I_{FULL \ LOAD}, 1.5V \le (V_{IN} - V_{OUT}) \le 15V$	1.225	1.250	1.270	V	
		(Note 7)				
V <sub>OUT</sub> Output Vo (Note 7)	Output Voltage	LM1085-3.3	3.270	3.300	3.330	v
	(Note 7)	$I_{OUT} = 0mA, V_{IN} = 5V$	3.270 3.235	3.300 3.300	3.365	v
		$0 \leq I_{OUT} \leq I_{FULL \ LOAD}, \ 4.8V \leq V_{IN} \leq 15V$	5.255	5.500	5.505	v
		LM1085-5.0	4.950	5.000	5.050	v
		$I_{OUT} = 0mA, V_{IN} = 8V$	4.900 4.900	5.000 5.000	5.000 5.100	v
		$0 \leq I_{OUT} \leq I_{FULL \ LOAD}, \ 6.5V \leq V_{IN} \leq 20V$	4.500	5.000	5.100	·
		LM1085-12	11.880	12.000	12.120	v
		$I_{OUT} = 0mA, V_{IN} = 15V$	11.760	12.000	12.120 12.240	v
	$0 \le I_{OUT} \le I_{FULL \ LOAD}, \ 13.5V \le V_{IN} \le 25V$	11.700	12.000	12.240	· ·	
	Line Regulation	LM1085-ADJ		0.015	0.2	%
	(Note 8)	$I_{OUT}$ =10mA, 1.5V $\leq$ (V <sub>IN</sub> -V <sub>OUT</sub> ) $\leq$ 15V		0.035	0.2	%
		LM1085-3.3		0.5	6	mV
		$I_{OUT}=0mA,\ 4.8V\leq V_{IN}\leq 15V$		1.0	6	mV
		LM1085-5.0		0.5	10	mV
		$I_{OUT}=0mA,\ 6.5V\leq V_{IN}\leq 20V$		1.0	10	mV
		LM1085-12		1.0	25	mV
		I <sub>OUT</sub> =0mA, $13.5V \le V_{IN} \le 25V$		2.0	25	mV
$\Delta V_{OUT}$	Load Regulation	LM1085-ADJ		0.1	0.3	%
	(Note 8)	$(V_{\text{IN}}\text{-}V_{\text{OUT}}) = 3V, \ 10mA \leq I_{\text{OUT}} \leq I_{\text{FULL LOAD}}$		0.2	0.4	%
		LM1085-3.3		3	15	mV
		$V_{IN}$ = 5V, 0 $\leq$ I <sub>OUT</sub> $\leq$ I <sub>FULL LOAD</sub>		7	20	mV
		LM1085-5.0		5	20	mV
		$V_{IN}$ = 8V, 0 ≤ $I_{OUT}$ ≤ $I_{FULL \ LOAD}$		10	35	mV
		LM1085-12		12	36	mV
		$V_{\text{IN}}$ = 15V, $0 \le I_{\text{OUT}} \le I_{\text{FULL LOAD}}$		24	72	mV
	Dropout Voltage	LM1085-ADJ, 3.3, 5, 12				
	(Note 9)	$\Delta V_{REF}$ , $\Delta V_{OUT}$ = 1%, $I_{OUT}$ = 3A		1.3	1.5	V

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#### Electrical Characteristics (Continued)

Typicals and limits appearing in normal type apply for  $T_J = 25^{\circ}C$ . Limits appearing in **Boldface** type apply over the entire junction temperature range for operation.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Unite
I <sub>LIMIT</sub> Current Limit	Current Limit	LM1085-ADJ		. ,	, ,	
		$V_{IN} - V_{OUT} = 5V$	3.2	5.5		A
		$V_{IN}-V_{OUT} = 25V$	0.2	0.5		A
		LM1085-3.3				
		$V_{IN} = 8V$	3.2	5.5		A
		LM1085-5.0				
		$V_{IN} = 10V$	3.2	5.5		A
		LM1085-12				
		V <sub>IN</sub> = 17V	3.2	5.5		A
	Minimum Load	LM1085-ADJ				
	Current (Note 10)	$V_{IN} - V_{OUT} = 25V$		5.0	10.0	mA
	Quiescent Current	LM1085-3.3				
		$V_{IN} \leq 18V$		5.0	10.0	mA
		LM1085-5.0				
		$V_{IN} \leq 20V$		5.0	10.0	mA
		LM1085-12				
Therm		$V_{IN} \le 25V$		5.0	10.0	mA
	Thermal Regulation	T <sub>A</sub> = 25°C, 30ms Pulse		.004	0.02	%/V
	Ripple Rejection	f <sub>RIPPLE</sub> = 120Hz, C <sub>OUT</sub> = 25µF Tantalum, I <sub>OUT</sub> = 3A				
		LM1085-ADJ, $C_{ADJ} = 25\mu F$ , $(V_{IN}-V_O) = 3V$	60	75		dB
		LM1085-3.3, V <sub>IN</sub> = 6.3V	60	72		dB
		LM1085-5.0, V <sub>IN</sub> = 8V	60	68		dB
		LM1085-12 V <sub>IN</sub> = 15V	54	60		dB
	Adjust Pin Current	LM1085		55	120	μA
	Adjust Pin Current	$10\text{mA} \le I_{\text{OUT}} \le I_{\text{FULL LOAD}}, 1.5\text{V} \le \text{V}_{\text{IN}} - \text{V}_{\text{OUT}} \le 25\text{V}$				P** *
	Change			0.2	5	μA
	Temperature			0.5		%
	Stability					, -
Long Ter RMS Out	Long Term Stability	T <sub>A</sub> =125°C, 1000Hrs		0.3	1.0	%
	RMS Output Noise	$10Hz \le f \le 10kHz$		0.003		%
	(% of V <sub>OUT</sub> )			0.000		,.
	Thermal Resistance	3-Lead TO-263: Control Section/Output Section			0.7/3.0	°C/V
	Junction-to-Case	3-Lead TO-220: Control Section/Output Section			0.7/3.0	°C/V

**Note 1:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics. **Note 2:** Power dissipation is kept in a safe range by current limiting circuitry. Refer to Overload Recovery in Application Notes.

Note 3: The maximum power dissipation is a function of  $T_{J(max)}$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is  $P_D = (T_{J(max)} - T_A)/\theta_{JA}$ . All numbers apply for packages soldered directly into a PC board. Refer to Thermal Considerations in the Application Notes.

**Note 4:** For testing purposes, ESD was applied using human body model,  $1.5k\Omega$  in series with 100pF.

Note 5: Typical Values represent the most likely parametric norm.

Note 6: All limits are guaranteed by testing or statistical analysis.

Note 7: IFULL LOAD is defined in the current limit curves. The IFULL LOAD Curve defines the current limit as a function of input-to-output voltage. Note that 30W power dissipation for the LM1085 is only achievable over a limited range of input-to-output voltage.

Note 8: Load and line regulation are measured at constant junction temperature, and are guaranteed up to the maximum power dissipation of 30W. Power dissipation is determined by the input/output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.

Note 9: Dropout voltage is specified over the full output current range of the device.

Note 10: The minimum output current required to maintain regulation.

